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DATE MAILED: 12/14/2004

	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
-	10/661,906	09/12/2003	Tomoaki Shino	790001-2036	8334	
	20999	7590 12/14/2004		EXAMINER		
		LAWRENCE & HAV	SMOOT, STEPHEN W			
	NEW YORK,			ART UNIT	PAPER NUMBER	
	,			2813		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	Application No. Applicant(s)						
		10/661,906		SHINO, TOMOAKI					
	Office Action Summary	Examiner		Art Unit					
		Stephen W.	Smoot	2813	av				
	The MAILING DATE of this communication app	pears on the	cover sheet with the	correspondence add	iress				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
2a)	· · · · · · · · · · · · · · · · · · ·								
Dispositi	on of Claims								
 4) ☐ Claim(s) 1-26 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) 1-18 is/are allowed. 6) ☐ Claim(s) 19-26 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement. 									
Applicati	on Papers			•					
 9) ☐ The specification is objected to by the Examiner. 10) ☒ The drawing(s) filed on 12 September 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 									
Priority u	ınder 35 U.S.C. § 119			•					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 									
2) Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date <u>9-12-2003</u> .		Interview Summar Paper No(s)/Mail [Notice of Informal Other:		-152)				

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DETAILED ACTION

This Office action is in response to application papers filed on 12 September 2003.

Drawings

1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated (see page 6, lines 2-3). Also see MPEP § 608.02(g).

Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.121(d)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

2. Claims 19, 22-23 are objected to because of the following informalities:

In claim 19, line 26, change "first MOS transistor" to --fourth MOS transistor-- for proper antecedence to claim 19, line 20;

In claim 22, line 5, change "active" to --active area-- for proper antecedence to claim 19, line 18; and

In claim 23, line 5, change "active" to --active area-- for proper antecedence to claim 19, line 18.

Appropriate correction is required.

Claim Rejections - 35 USC § 112

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claims 19-26 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 19, line 9, the term "the diffusion layer" does not particularly point out if it is in reference to the diffusion layer connected to a contact from lines 5-6 or to the diffusion layer on a node side from line 7;

In claim 19, line 27, the term "the diffusion layer" does not particularly point out if it is in reference to the diffusion layer connected to a contact from lines 23-24 or to the diffusion layer on a node side from line 25; and

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Claims 20-26 are rejected under 35 U.S.C. 112, second paragraph, because they depend on claim 19.

Allowable Subject Matter

- 5. Claims 1-18 are allowed.
- 6. The following is a statement of reasons for the indication of allowable subject matter:
 - Claims 1-9 are allowed because the prior art of record does not teach or suggest, in combination with the other claim limitations, a semiconductor device that includes a first gate electrode that functions as a gate of a first conductive type of first MOS transistor in a first active area and also functions as a gate of a second conductive type of second MOS transistor in a second active area, wherein an end portion of the first gate electrode projecting from the first active area is obliquely arranged relative to a gate width direction of the first MOS transistor;
 - Claims 10-18 are allowed because the prior art of record does not teach or suggest, in combination with the other claim limitations, a semiconductor device that includes a first gate electrode that functions as a gate of a first conductive type of first MOS transistor in a first active area formed in an isolation area and also functions as a gate of a second conductive type of second MOS transistor in

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a second active area formed in the isolation area, wherein an end portion of the first gate electrode is present in the isolation area and is obliquely arranged relative to an extended direction of the first gate electrode arranged on the first active area.

Conclusion

- 7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Perera et al. teach an SRAM that features angled gates. Chan et al., Wada et al., Lee, and Nii teach CMOS memory cells that feature six transistors.
- 8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen W. Smoot whose telephone number is 571-272-1698. The examiner can normally be reached on M-F (8:00 am to 4:30 pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr. can be reached on 571-272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SWS

Stephen W. Smoot Patent Examiner Art Unit 2813